

| Ref # | Hits | Search Query  | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|---|---|------------------|---------|------------------|
| L1    | 119  | pla\$2 and GLA\$2 and PLD\$2 and FPGA\$2 and (programmable near (logic device))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | OFF     | 2005/09/23 16:19 |
| L3    | 9    | pla\$2 and GLA\$2 and PLD\$2 and FPGA\$2 and (programmable near (logic device)) and configurable  | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 16:20 |
| L6    | 8    | pla\$2 and GLA\$2 and PLD\$2 and FPGA\$2 and (programmable near (logic device)) and (configurable and interface)  | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 17:32 |
| L7    | 13   | "5870619"   | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 17:13 |
| L8    | 3    | ("6598148").URPN.   | USPAT   | OR               | OFF     | 2005/09/23 17:20 |
| L12   | 45   | (pla\$2 or GLA\$2 or PLD\$2 or FPGA\$2) and ((programmable near (logic device)) with (configurable with interface))   | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 17:33 |
| L13   | 16   | (pla\$2 or GLA\$2 or PLD\$2 or FPGA\$2) same ((programmable near (logic device)) with (configurable with interface))  | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 19:04 |
| L14   | 1    | (712/32 712/37 712/20 712/23 710/266 710/72) and (pla\$2 or GLA\$2 or PLD\$2 or FPGA\$2) same ((programmable near (logic device)) with (configurable with interface)) | USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB              | OR               | OFF     | 2005/09/23 19:10 |